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Application Number:	10581035
International Application Number:	
Confirmation Number:	5997
Title of Invention:	THIN FILM TRANSISTOR INTEGRATED CIRCUIT DEVICE, ACTIVE MATRIX DISPLAY DEVICE, AND MANUFACTURING METHODS OF THE SAME
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1		50160103PUS1IssuefeeandIDS. PDF	508979 aa10fa9874df2dd244a54af9a4fa4a6515ca29c7	yes	9
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Issue Fee Payment (PTO-85B)		1	1	
	Miscellaneous Incoming Letter		2	2	
	Information Disclosure Statement (IDS) Form (SB08)		3	9	
Warnings:					
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2	Non Patent Literature	IDSJPOA2005516083dated20110803.pdf	171297 1cf7f084864125e6741d27b15019bdf5812b6115	no	4
Warnings:					
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3	Non Patent Literature	EnglishTranslationofJPOA.PDF	141706 eb3f5e1630073fe4a5be35772aa70d52489686c9	no	3
Warnings:					
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4	Foreign Reference	JP2001318622.pdf	1489745 6d8b95f00a4b7360ed36b4f92535f6d970487e16	no	18
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5	Fee Worksheet (SB06)	fee-info.pdf	33827 d4809917181f37b8c2600dcb54ecfa6979ffc6ac	no	2
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National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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